

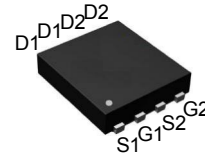
Features

- Channel 1**
 30V/18A,
 $R_{DS(ON)} = 7\text{ m}\Omega$ (typ.) @ $V_{GS} = 10\text{V}$
 $R_{DS(ON)} = 10\text{m}\Omega$ (typ.) @ $V_{GS} = 4.5\text{V}$
- Channel 2**
 30V/18A,
 $R_{DS(ON)} = 7\text{ m}\Omega$ (typ.) @ $V_{GS} = 10\text{V}$
 $R_{DS(ON)} = 10\text{m}\Omega$ (typ.) @ $V_{GS} = 4.5\text{V}$
- 100% UIS Tested
- Reliable and Rugged
- Lead Free Available (RoHS Compliant)

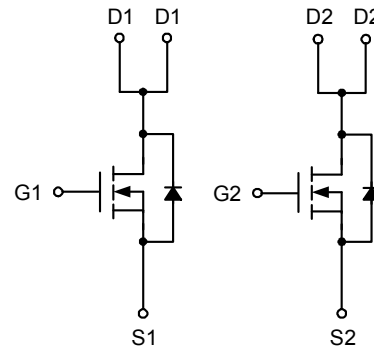
Applications

- Power Management in Desktop Computer or DC/DC Converters.

Pin Description



Top View of DFN3.3x3.3-8-EP



N-Channel MOSFET

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Channel 1	Channel 2	Unit	
Common Ratings					
V_{DSS}	Drain-Source Voltage	30	30	V	
V_{GSS}	Gate-Source Voltage	± 20	± 20		
T_J	Maximum Junction Temperature	150		$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55 to 150			
I_S	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	18	18	A
I_D^a	Continuous Drain Current	$T_C=25^\circ\text{C}$	18	18	A
I_{DM}^b	Pulse Drain Current Tested	$T_C=25^\circ\text{C}$	54	54	A
P_D	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	20	20	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	6	6	$^\circ\text{C}/\text{W}$
I_D^c	Continuous Drain Current	$T_A=25^\circ\text{C}$	8.4	9.1	A
		$T_A=70^\circ\text{C}$	6.7	7.3	
I_{DM}	Pulse Drain Current Tested	$T_A=25^\circ\text{C}$	33.5	36	A
P_D^c	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	1.14	1.3	W
		$T_A=70^\circ\text{C}$	0.7	0.8	
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$	66	60	$^\circ\text{C}/\text{W}$
		Steady State ^c	110	100	
I_{AS}^d	Avalanche Current, Single pulse	$L=0.1\text{mH}$	15	15	A
		$L=0.5\text{mH}$	9	9	
E_{AS}^d	Avalanche Energy, Single pulse	$L=0.1\text{mH}$	11.25	11.25	mJ
		$L=0.5\text{mH}$	20.3	20.3	

Note a,* : Max. continuous current is limited by bonding wire.

Note b : Pulse width is limited by max. junction temperature.

Note c : $R_{\theta JA}$ steady state $t=999\text{s}$.

Note d : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J=25^\circ\text{C}$).

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Channel 1			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$	-	-	1	μA
		$T_J=85^\circ C$	-	-	30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	1.4	1.8	2.5	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
$R_{DS(ON)}^e$	Drain-Source On-state Resistance	$V_{GS}=10V, I_{DS}=10A$	-	7	10	m Ω
		$V_{GS}=4.5V, I_{DS}=8A$	-	10	15	
Diode Characteristics						
V_{SD}^e	Diode Forward Voltage	$I_{SD}=5A, V_{GS}=0V$	-	0.8	1.1	V
t_{rr}	Reverse Recovery Time	$I_{DS}=10A, di_{SD}/dt=100A/\mu s$	-	20.5	-	ns
Q_{rr}	Reverse Recovery Charge		-	7.2	-	nC
Dynamic Characteristics^f						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	-	1.35	2.5	Ω
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=15V,$ Frequency=1.0MHz	-	455	600	pF
C_{oss}	Output Capacitance		-	318	-	
C_{rss}	Reverse Transfer Capacitance		-	22	-	
$t_{d(ON)}$	Turn-on Delay Time		$V_{DD}=15V, R_L=15\Omega,$ $I_{DS}=1A, V_{GEN}=10V,$ $R_G=6\Omega$	-	8.5	16
t_r	Turn-on Rise Time	-		10	18	
$t_{d(OFF)}$	Turn-off Delay Time	-		14	26	
t_f	Turn-off Fall Time	-		10.6	19	
Gate Charge Characteristics^f						
Q_g	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V,$ $I_{DS}=10A$	-	8	12	nC
Q_{gs}	Gate-Source Charge		-	1.6	-	
Q_{gd}	Gate-Drain Charge		-	1.2	-	

 Note e : Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

Note f : Guaranteed by design, not subject to production testing.

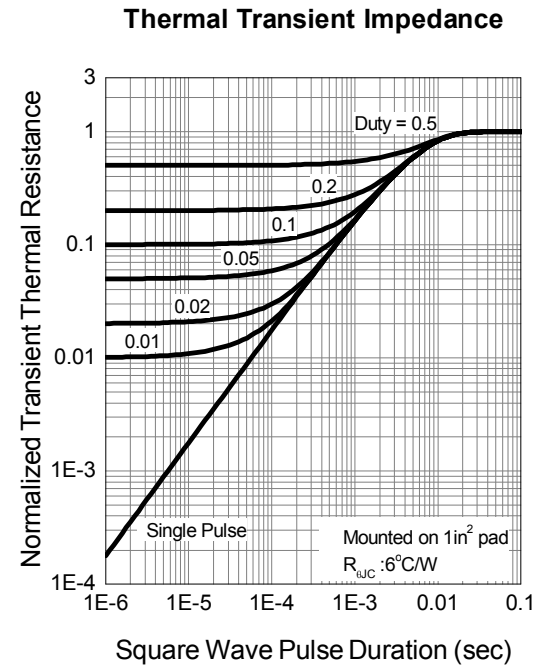
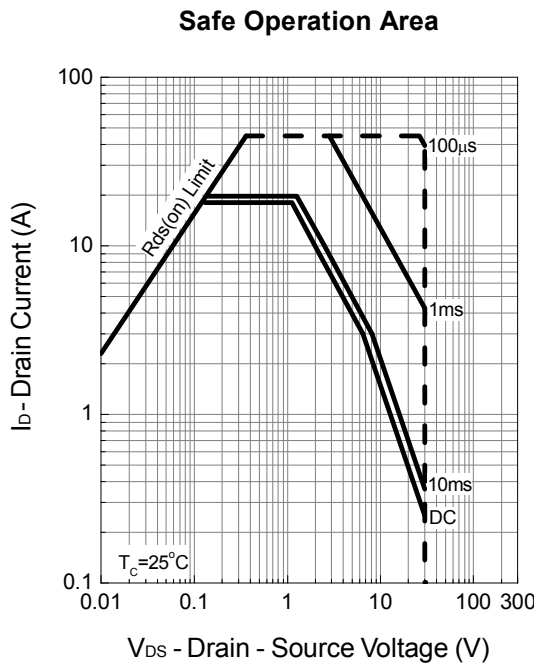
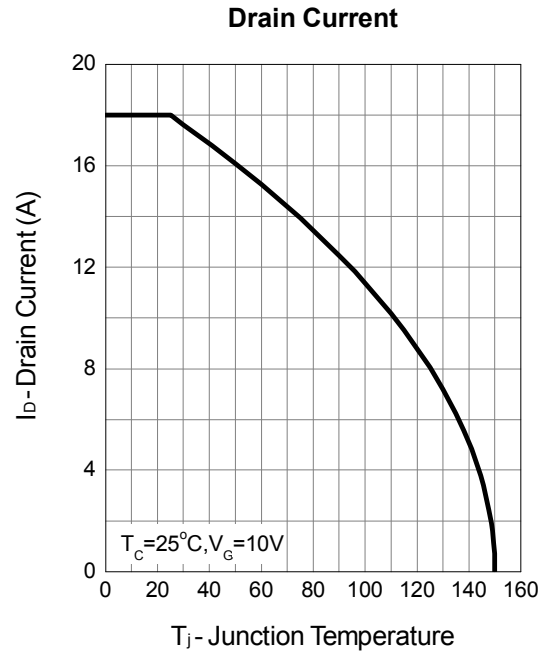
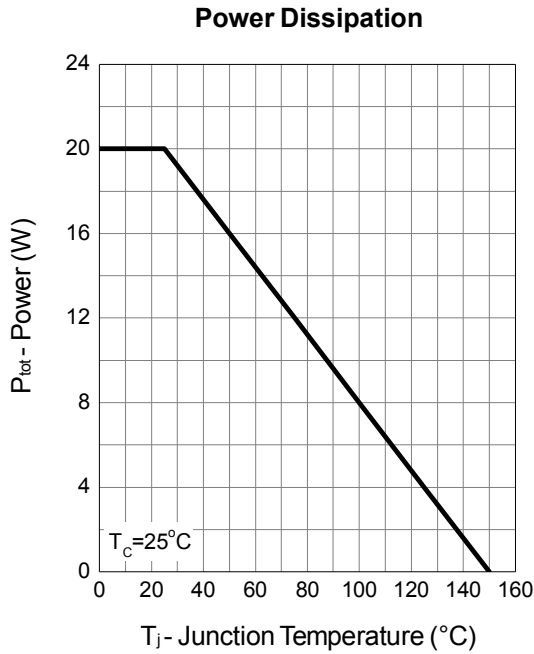
Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Channel 2			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$	-	-	1	μA
		$T_J=85^\circ C$	-	-	30	mA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=-250\mu A$	1.3	1.8	2.5	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
$R_{DS(ON)}^e$	Drain-Source On-state Resistance	$V_{GS}=10V, I_{DS}=10A$	-	7	10	m Ω
		$V_{GS}=4.5V, I_{DS}=8A$	-	10	15	
Diode Characteristics						
V_{SD}^e	Diode Forward Voltage	$I_{SD}=5A, V_{GS}=0V$	-	0.8	1.3	V
t_{rr}	Reverse Recovery Time	$I_{DS}=10A, di_{SD}/dt=100A/\mu s$	-	20.5	-	ns
Q_{rr}	Reverse Recovery Charge		-	7.2	-	nC
Dynamic Characteristics^f						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	-	1.35	2.5	Ω
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, Frequency=1.0MHz$	-	455	600	pF
C_{oss}	Output Capacitance		-	318	-	
C_{rss}	Reverse Transfer Capacitance		-	22	-	
$t_{d(ON)}$	Turn-on Delay Time		$V_{DD}=15V, R_L=15\Omega, I_{DS}=1A, V_{GEN}=10V, R_G=6\Omega$	-	8.5	16
t_r	Turn-on Rise Time	-		10	18	
$t_{d(OFF)}$	Turn-off Delay Time	-		14	26	
t_f	Turn-off Fall Time	-		10.6	19	
Gate Charge Characteristics^f						
Q_g	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V, I_{DS}=10A$	-	8	12	nC
Q_{gs}	Gate-Source Charge		-	1.6	-	
Q_{gd}	Gate-Drain Charge		-	1.2	-	

 Note e : Pulse test; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

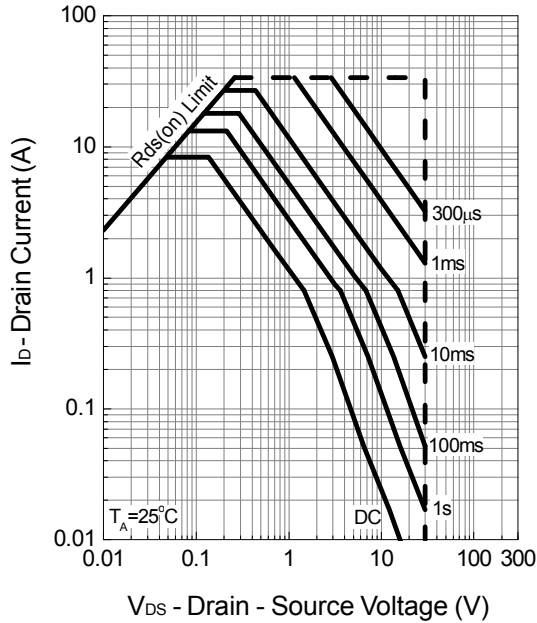
Note f : Guaranteed by design, not subject to production testing.

Channel 1 Typical Operating Characteristics

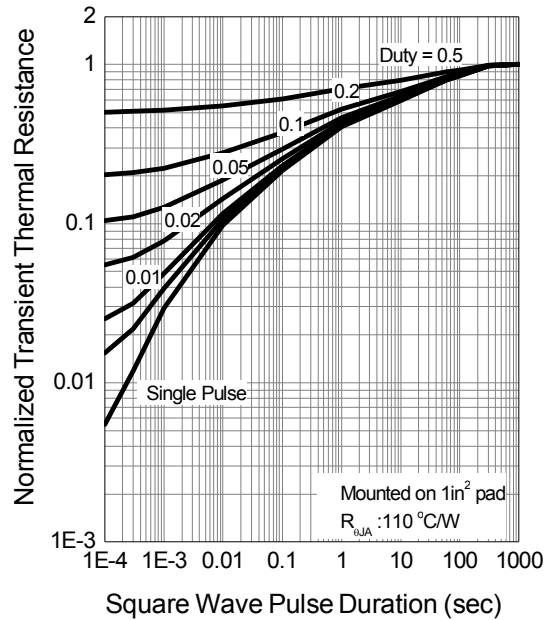


Channel 1 Typical Operating Characteristics (Cont.)

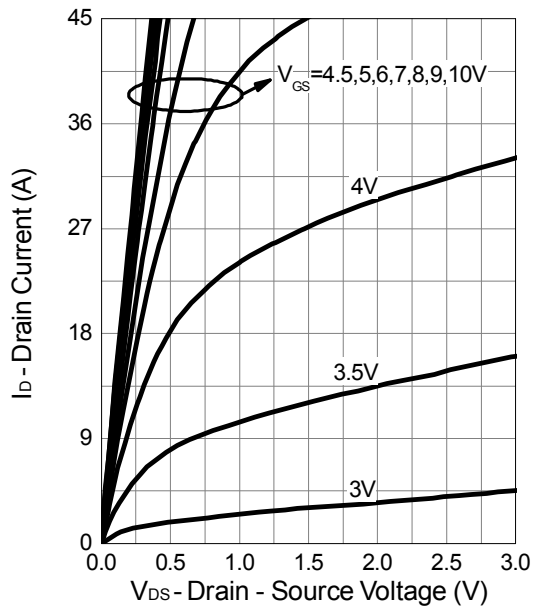
Safe Operation Area



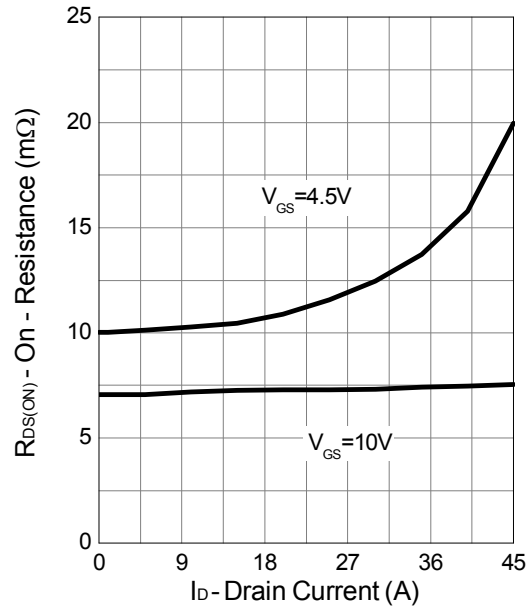
Thermal Transient Impedance



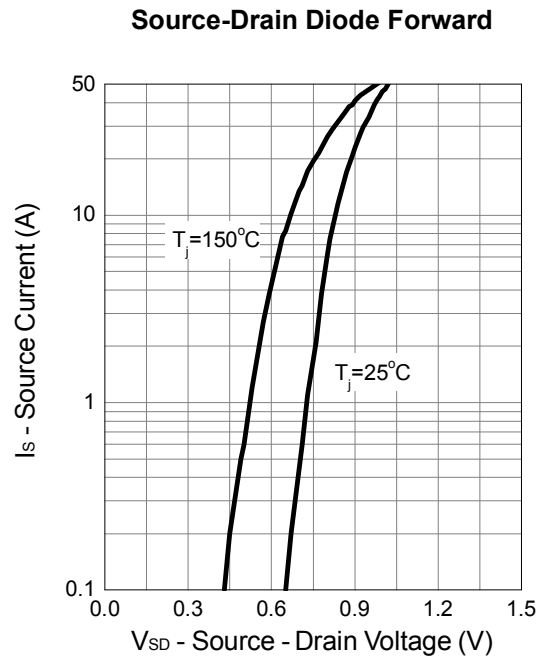
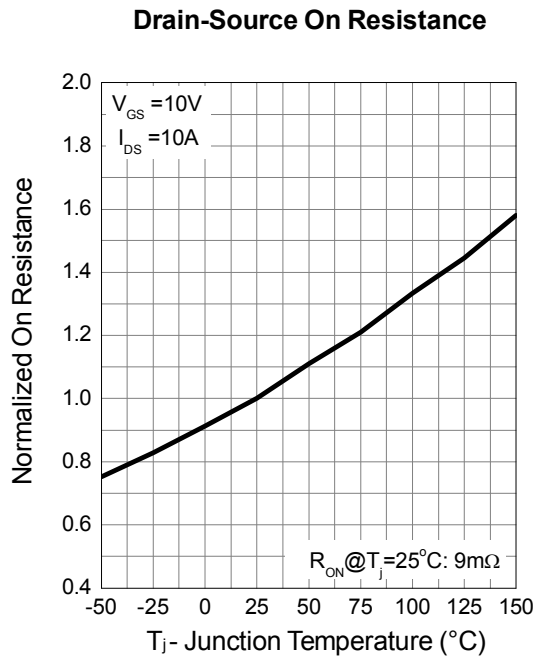
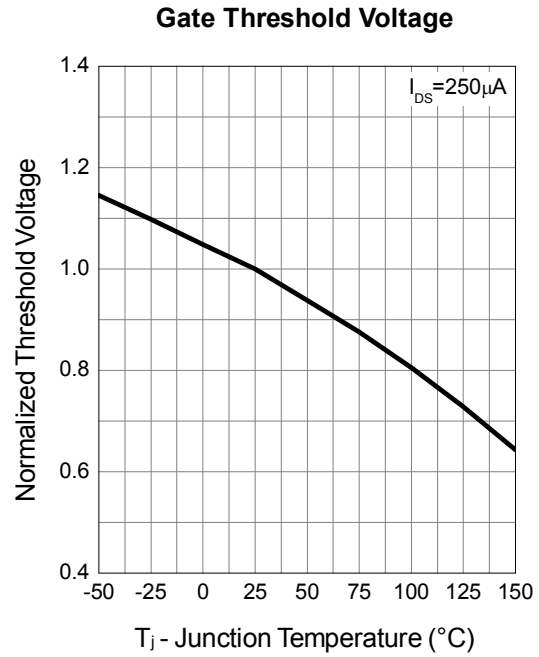
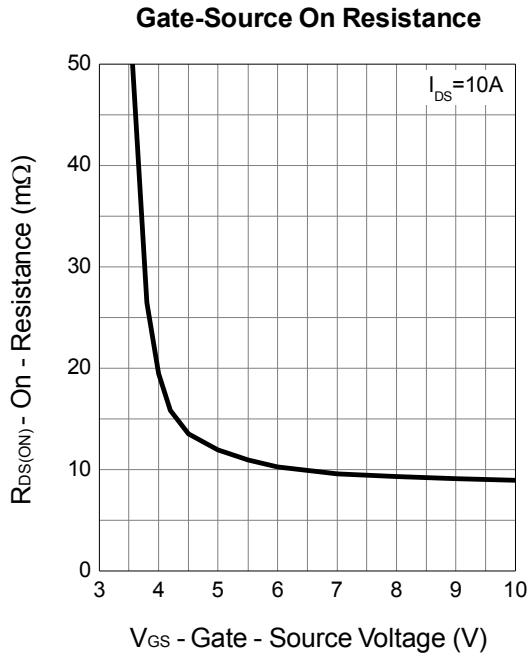
Output Characteristics



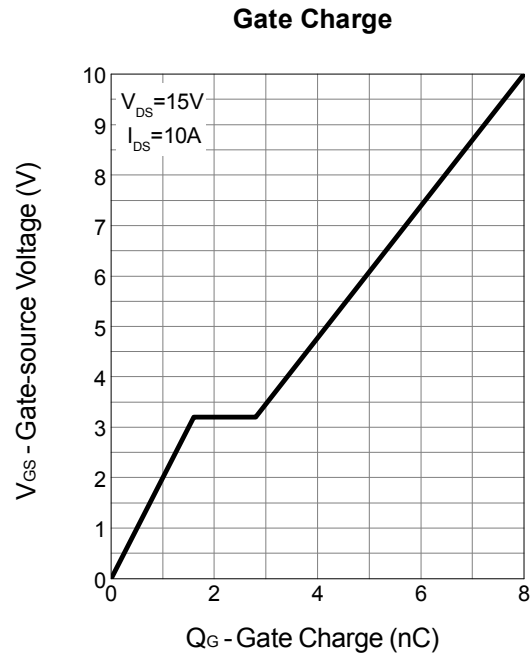
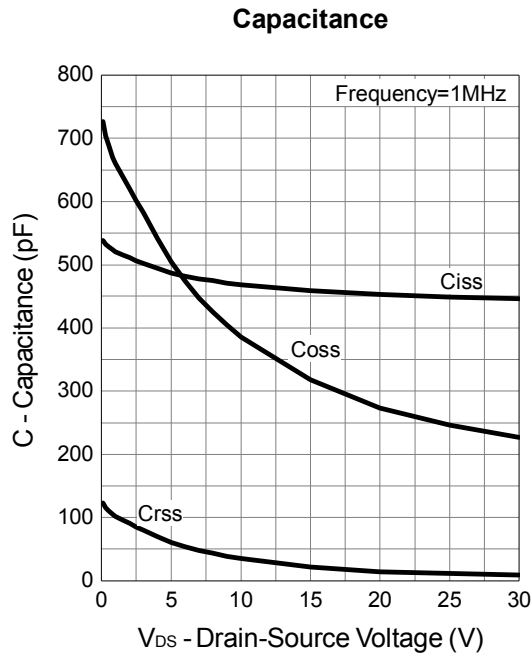
Drain-Source On Resistance



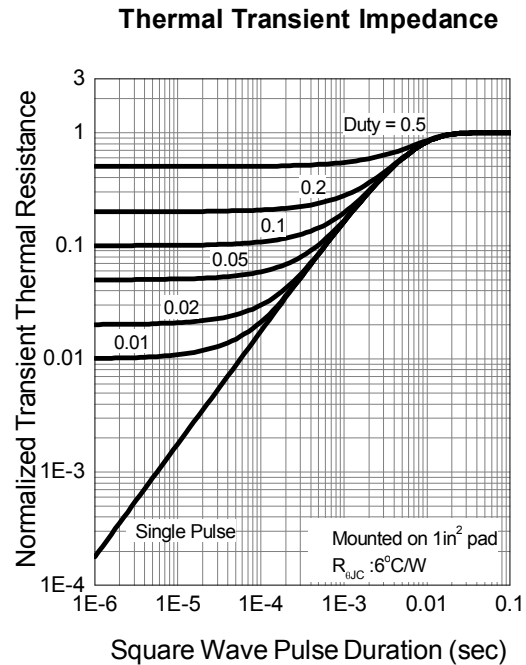
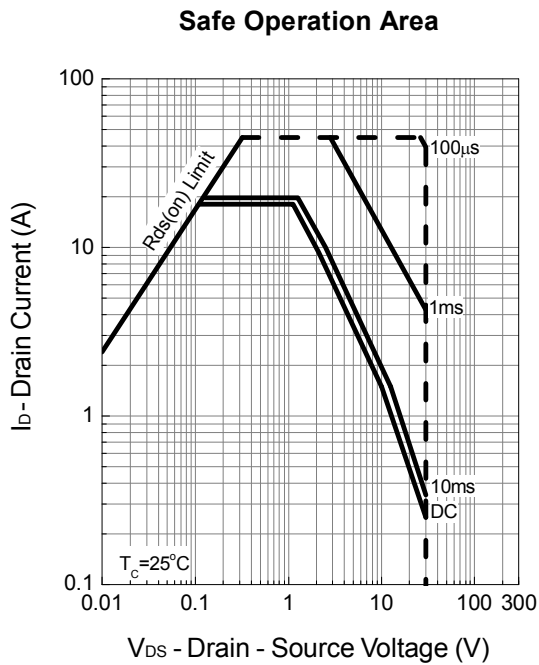
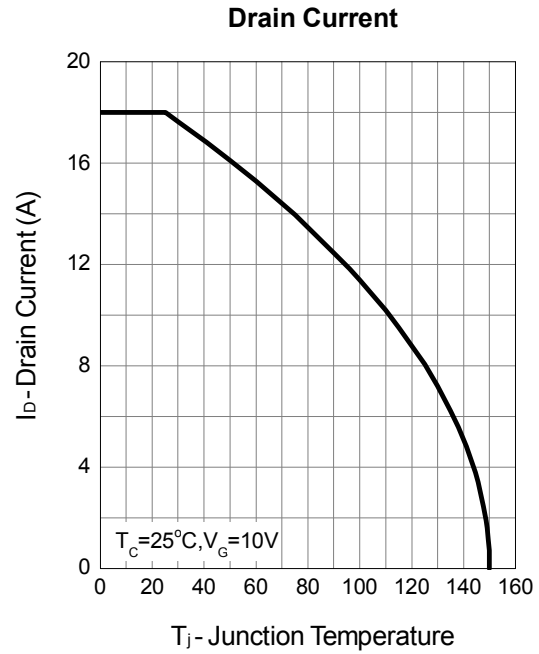
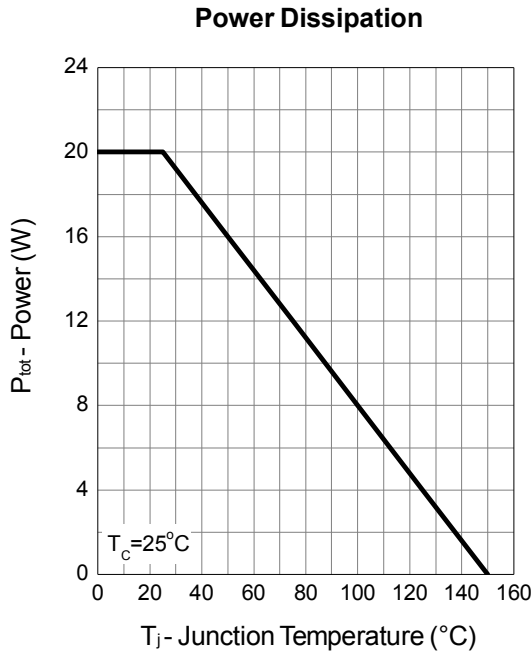
Channel 1 Typical Operating Characteristics (Cont.)



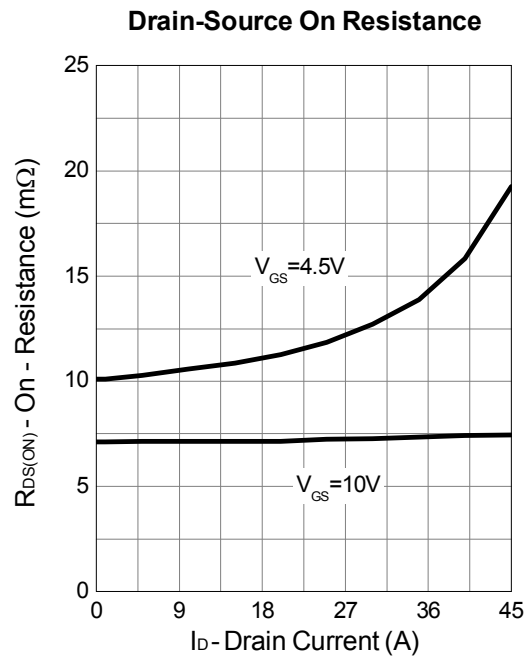
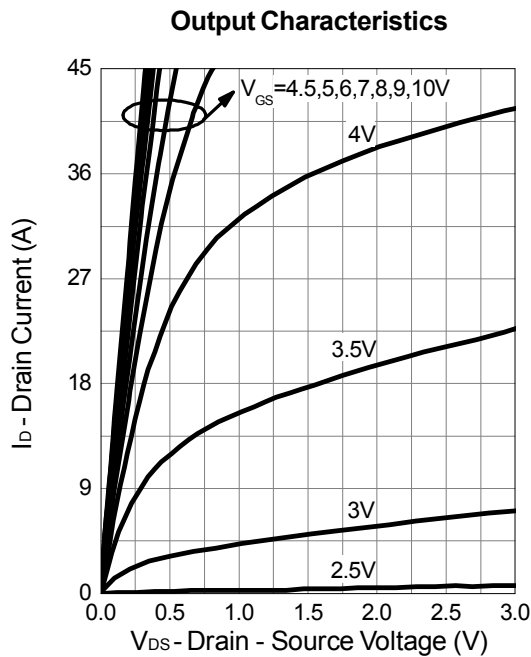
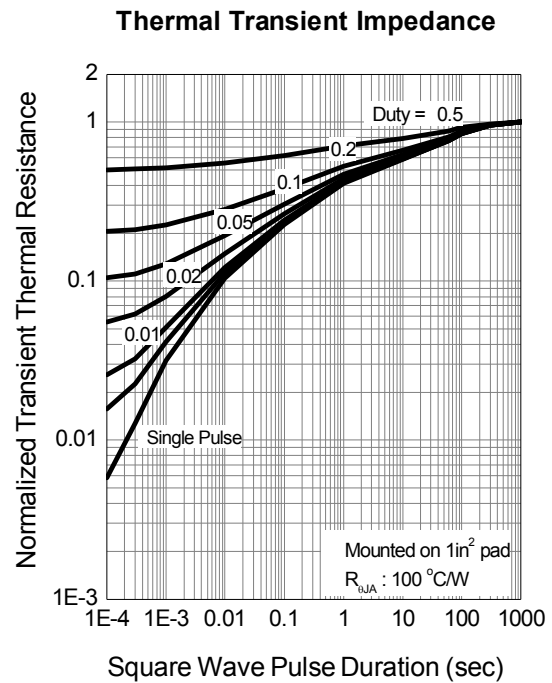
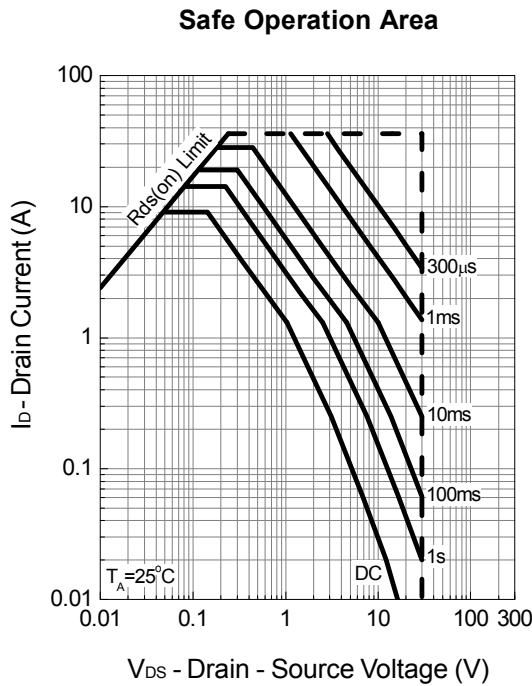
Channel 1 Typical Operating Characteristics (Cont.)



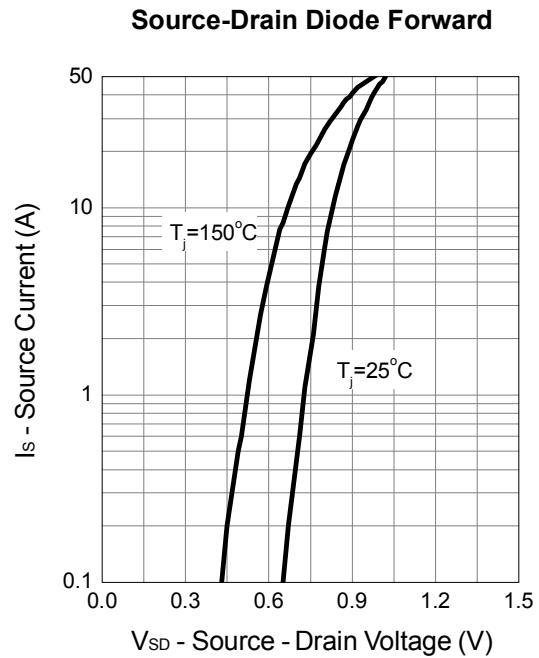
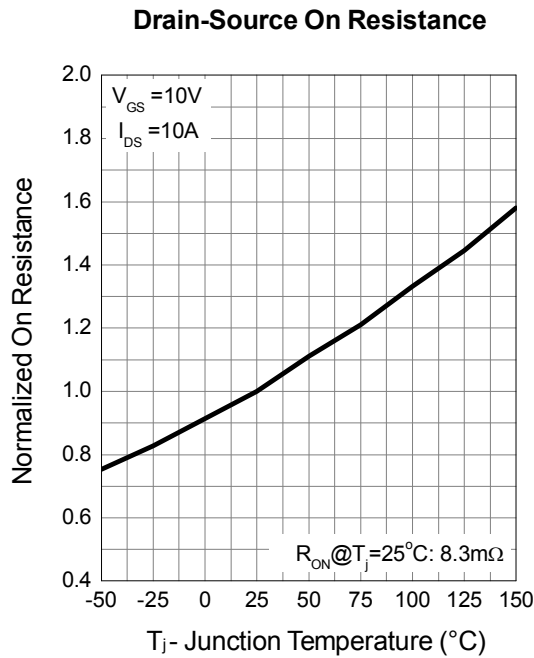
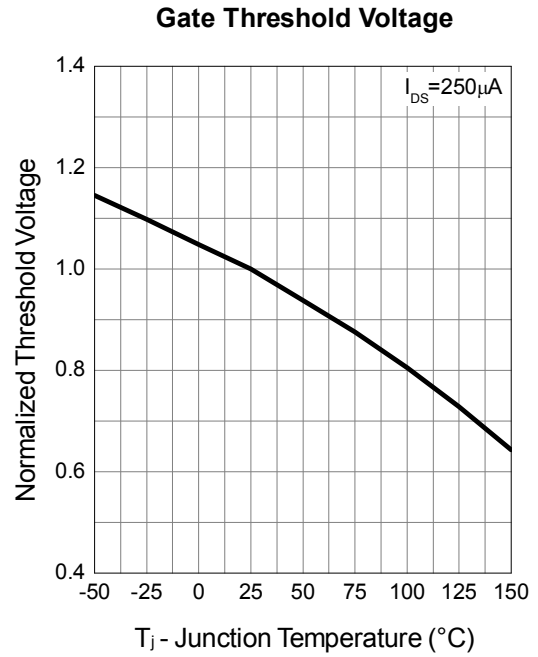
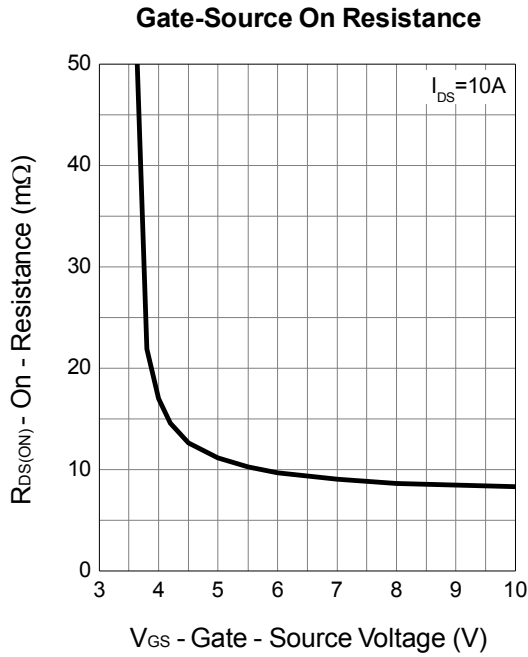
Channel 2 Typical Operating Characteristics



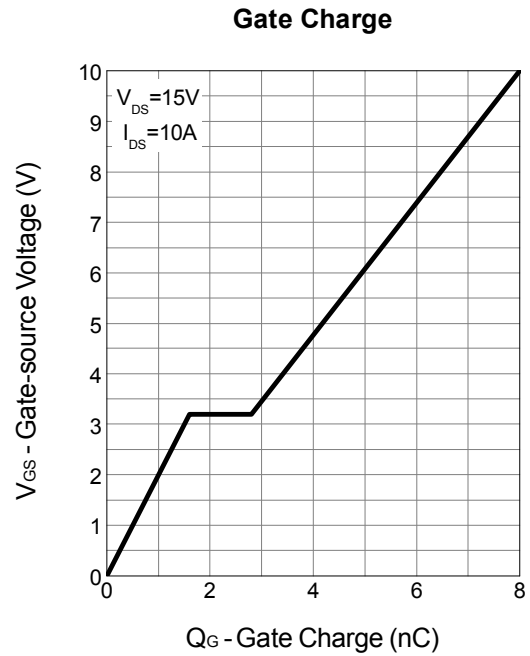
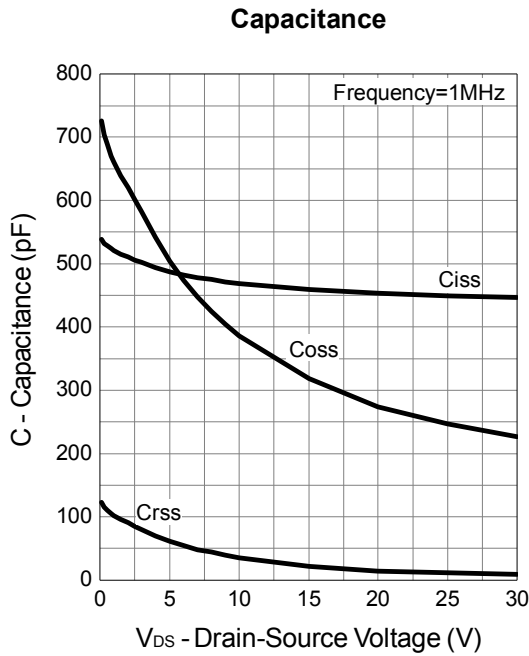
Channel 2 Typical Operating Characteristics (Cont.)



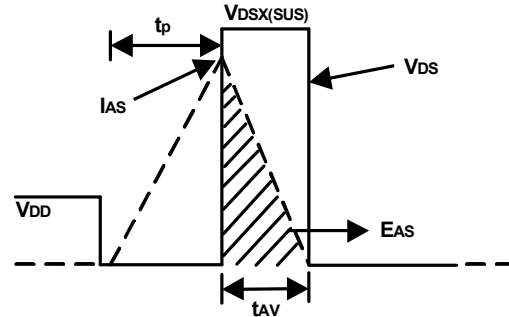
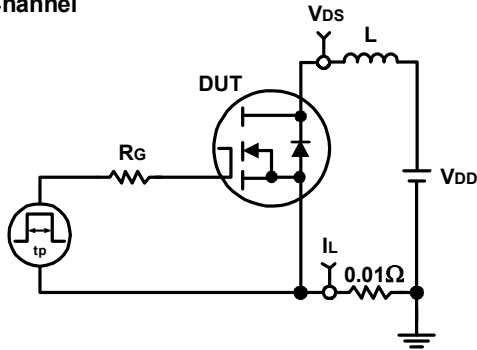
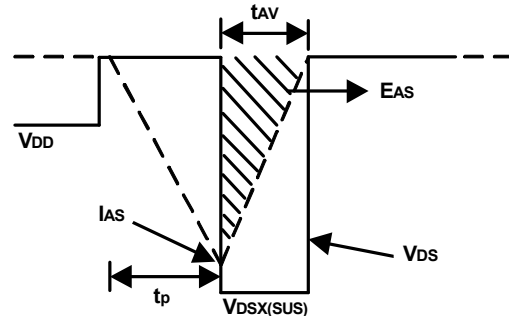
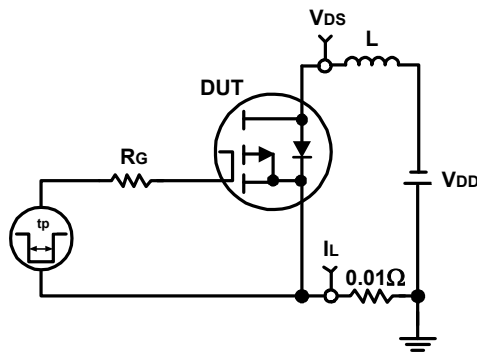
Channel 2 Typical Operating Characteristics (Cont.)



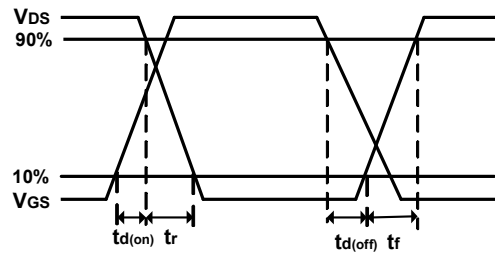
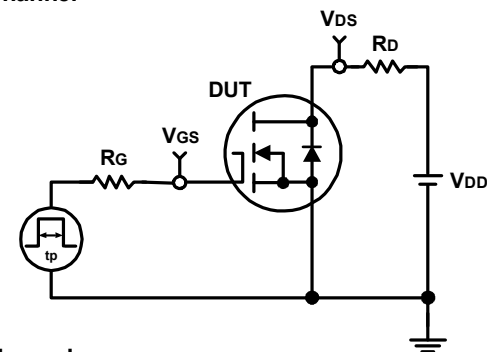
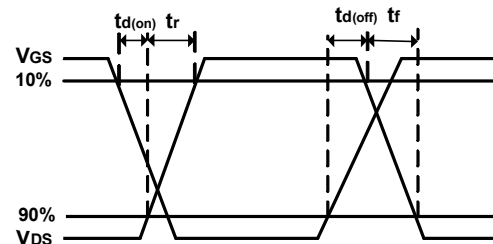
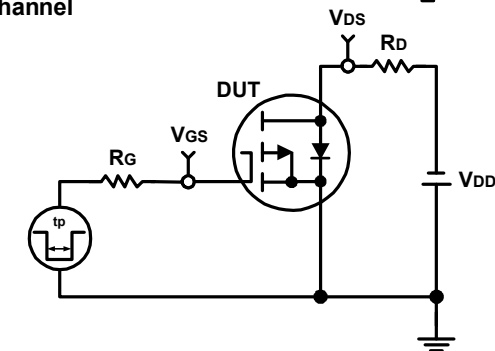
Channel 2 Typical Operating Characteristics (Cont.)



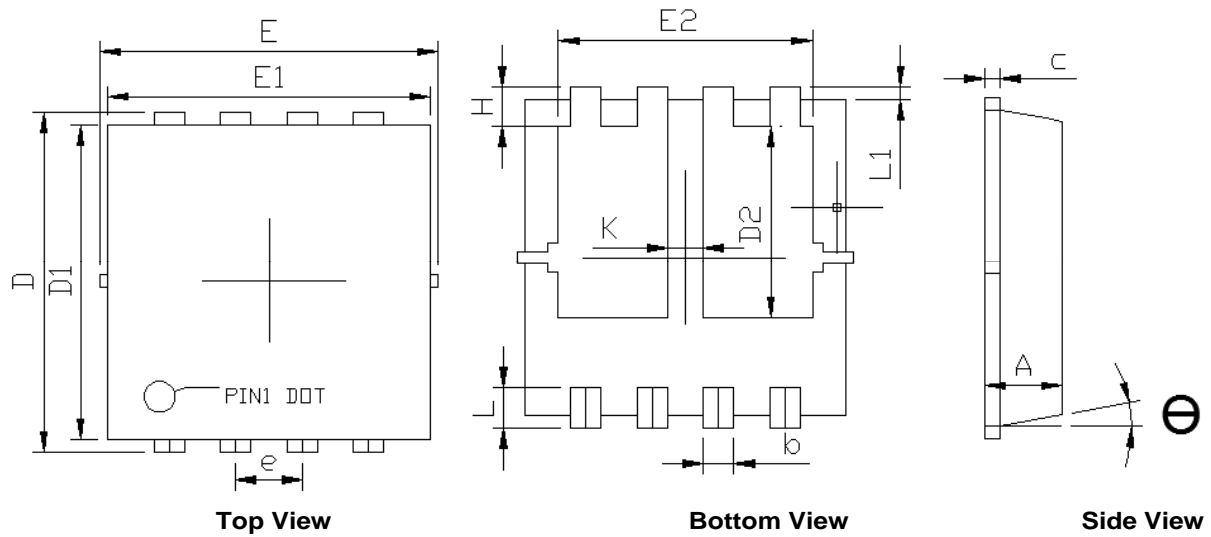
Avalanche Test Circuit and Waveforms

N Channel

P Channel


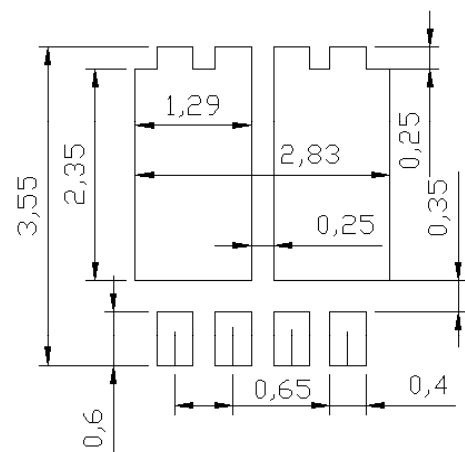
Switching Time Test Circuit and Waveforms

N Channel

P Channel


Package Information

DFN3.3x3.3-8L_EP2_P


SYMBOLS	DFN3.3x3.3G-8_EP2_P			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.70	0.80	0.028	0.031
b	0.25	0.35	0.010	0.014
c	0.10	0.25	0.004	0.010
D	3.25	3.45	0.128	0.136
D1	3.00	3.20	0.118	0.126
D2	1.78	1.98	0.070	0.078
E	3.20	3.40	0.126	0.134
E1	3.00	3.20	0.118	0.126
E2	2.39	2.59	0.094	0.102
e	0.65BSC		0.026 BSC	
H	0.30	0.50	0.012	0.020
L	0.30	0.50	0.012	0.020
L1	0.13 REF		0.005 REF	
K	0.30	-	0.012	-
θ	-	12°	-	12°

RECOMMENDED LAND PATTERN


UNIT: mm