

Features

- Uses advanced SGT technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent gate charge x $R_{DS(on)}$ product(FOM)

Application

- Motor control and drive
- Battery management
- DC/DC

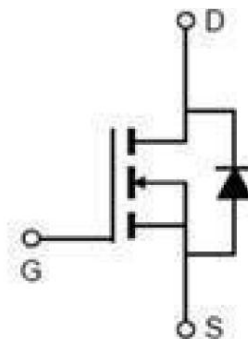
Product Summary

Package	PDFN5*6
V_{DS}	100V
$R_{DS(on)}@V_{GS}=10V$	2.2mΩ
I_D	219A

Package Marking



PDFN5X6



Type	Package	Marking	Reel Size	Tape Width	Packing	Qty
LU028N10XT8	PDFN5*6	LU028N10XT8	330*16.5mm	12mm	Reel&Tape	5000

Maximum Ratings (TC=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous drain current $T_c=25^\circ\text{C}$ (Silicon limit) $T_c=100^\circ\text{C}$ (Silicon limit)	I_D	219 139	A
Pulsed Drain Current	I_{DM}	1092	A
Maximum Power Dissipation	P_d	215	W
Single pulse avalanche energy	E_{AS}	1140	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance,Junction-to-Case	$R_{\theta JC}$	0.58	°C/W
Thermal resistance, junction – Ambient	$R_{\theta JA}$	50	

Electrical Characteristics (T_c=25°C unless otherwise noted)

Parameter	Symbol	Condition	min	typ	max	Unit
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Static Characteristic

Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	100	106	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	2.9	4	
Zero gate voltage drain current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$	-	-	1	μA
Gate-source leakage current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	±100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$I_D=20A, V_{GS}=10V$	-	2.2	2.8	mΩ

Dynamic Characteristic

Input Capacitance	C_{iss}	$V_{DS}=50V, V_{GS}=0V,$ $f=1MHz$	-	3550	-	pF
Output Capacitance	C_{oss}		-	1621	-	
Reverse Transfer Capacitance	C_{rss}		-	34	-	
Gate resistance	R_g	$f=1MHz$		2.2		Ω
Gate Total Charge	Q_G	$V_{DS}=50V, I_D=50A,$ $V_{GS}=10V$	-	107	-	nC
Gate-Source charge	Q_{gs}		-	32	-	
Gate-Drain charge	Q_{gd}		-	23	-	
Turn-on delay time	$t_{d(on)}$	$V_{GS}=10V, V_{DS}=50V,$ $R_G=3\Omega$	-	15	-	ns
Rise time	t_r		-	6	-	
Turn-off delay time	$t_{d(off)}$		-	28	-	
Fall time	t_f		-	7	-	

Body Diode Characteristic

Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=2A$	-	-	1.2	v
Reverse Recovery Time	t_{rr}	$I_F = 100A$ $di/dt = 100A/\mu s$	-	48	-	ns
Reverse Recovery Charge	Q_{rr}		-	71	-	nC

Typical Performance Characteristics

Diagram 1: Power dissipation

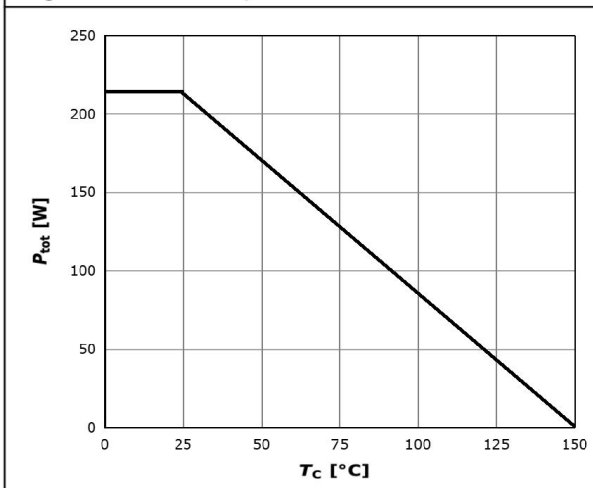


Diagram 2: Drain current

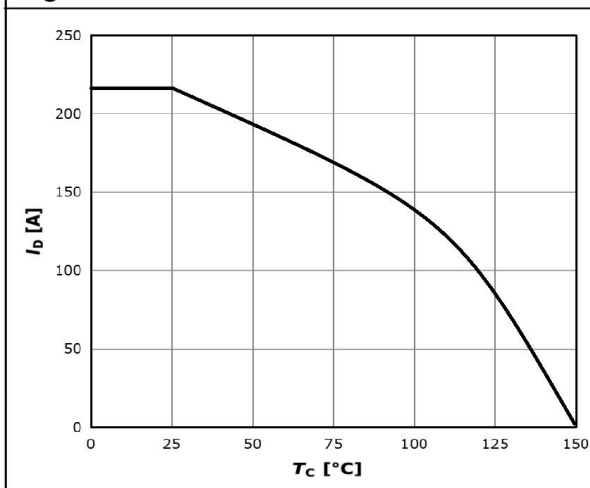


Diagram 3: Safe operating area

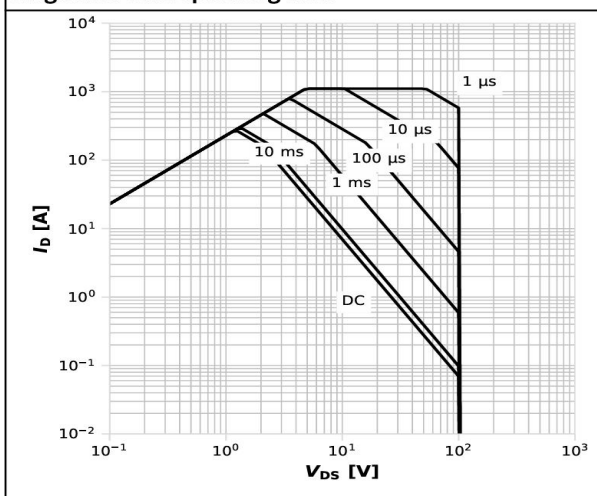


Diagram 4: Max. transient thermal impedance

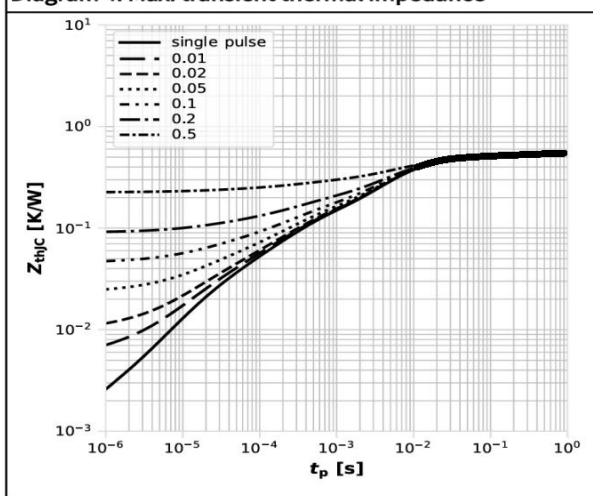


Diagram 5: Typ. output characteristics

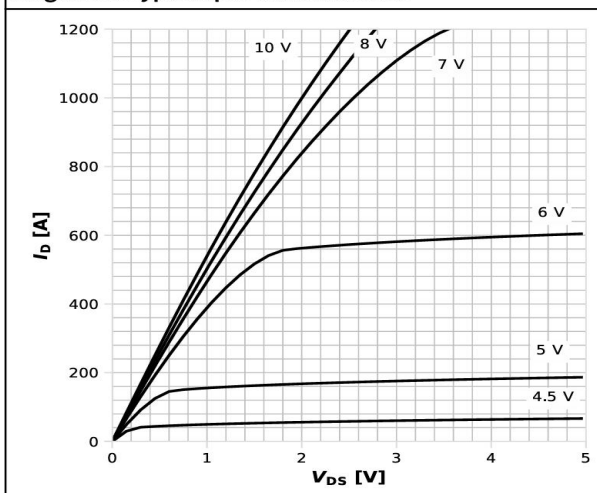


Diagram 6: Typ. transfer characteristics

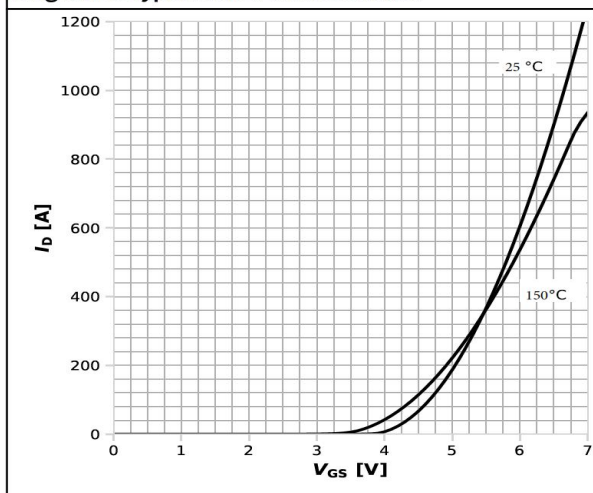


Diagram 7: Normalized drain-source on resistance

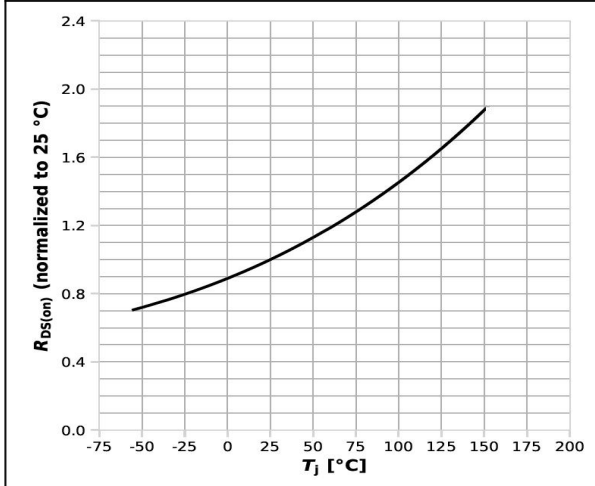


Diagram 8: Typ. drain-source on resistance

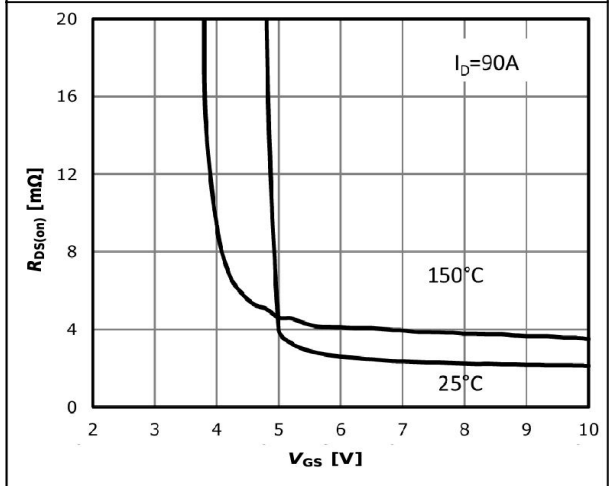


Diagram 9: Forward characteristics of reverse diode

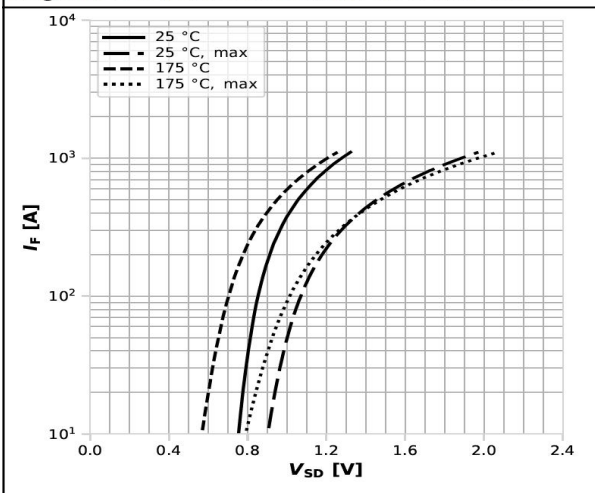


Diagram 10: Min. drain-source breakdown voltage

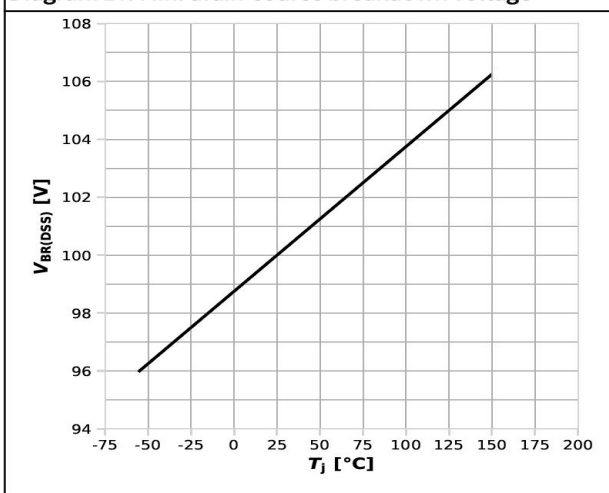


Diagram 11: Typ. capacitances

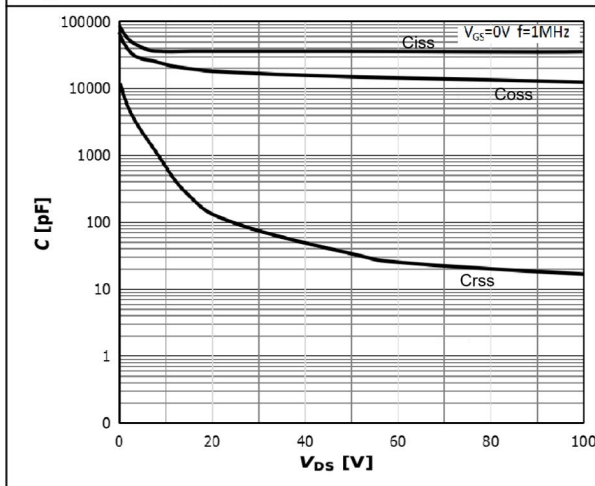
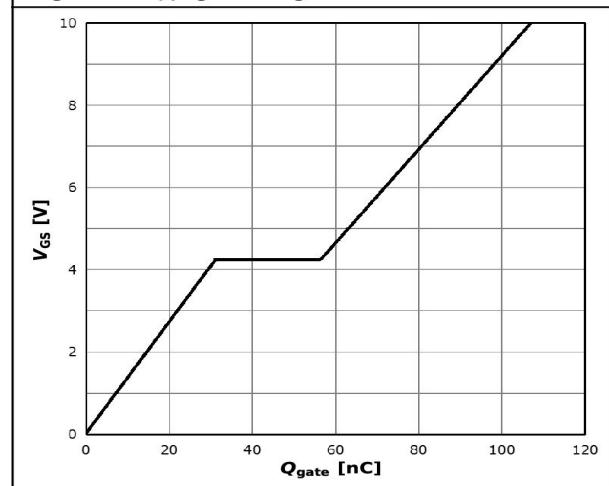
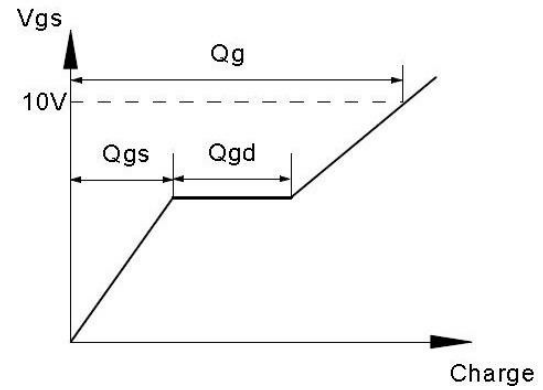
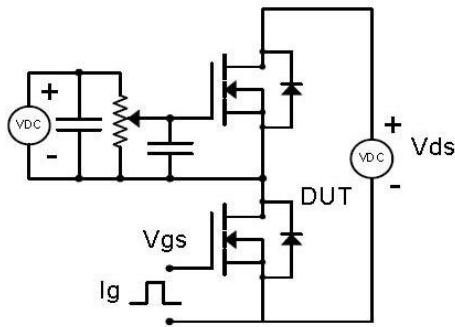


Diagram 12: Typ. gate charge

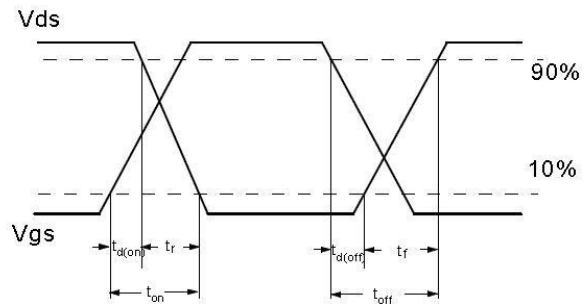
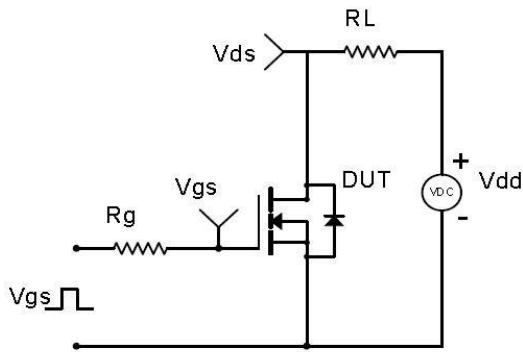


Test Circuit & Waveform

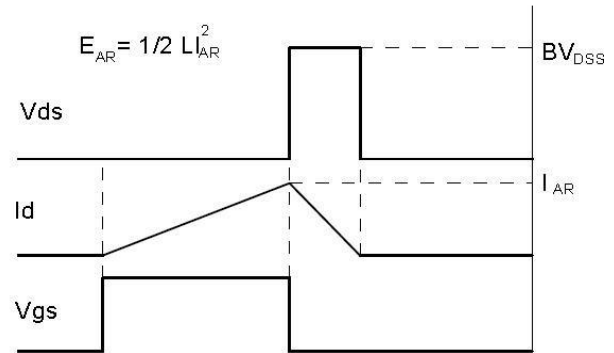
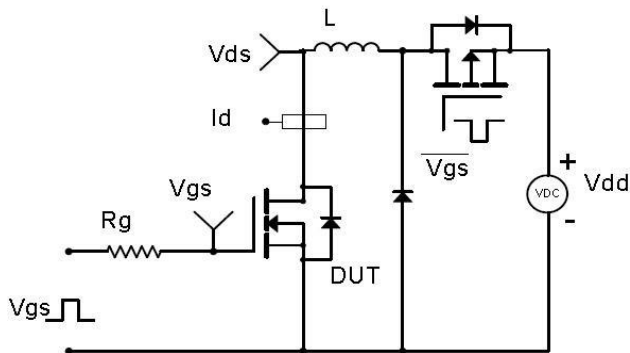
Gate Charge Test Circuit & Waveform



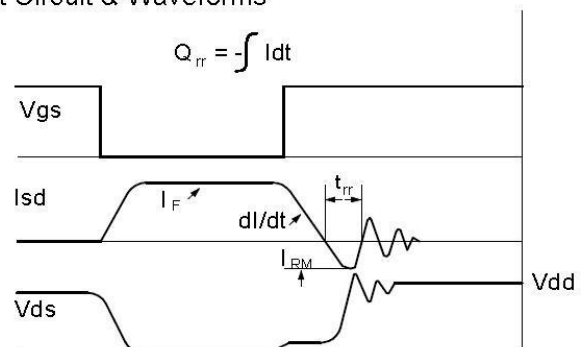
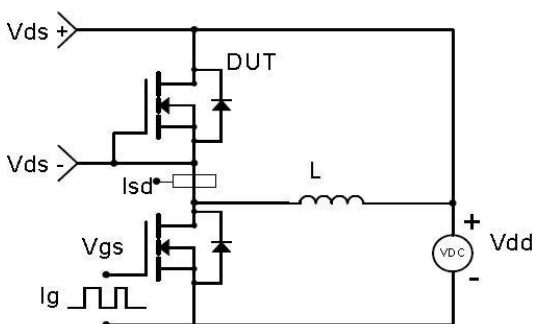
Resistive Switching Test Circuit & Waveforms



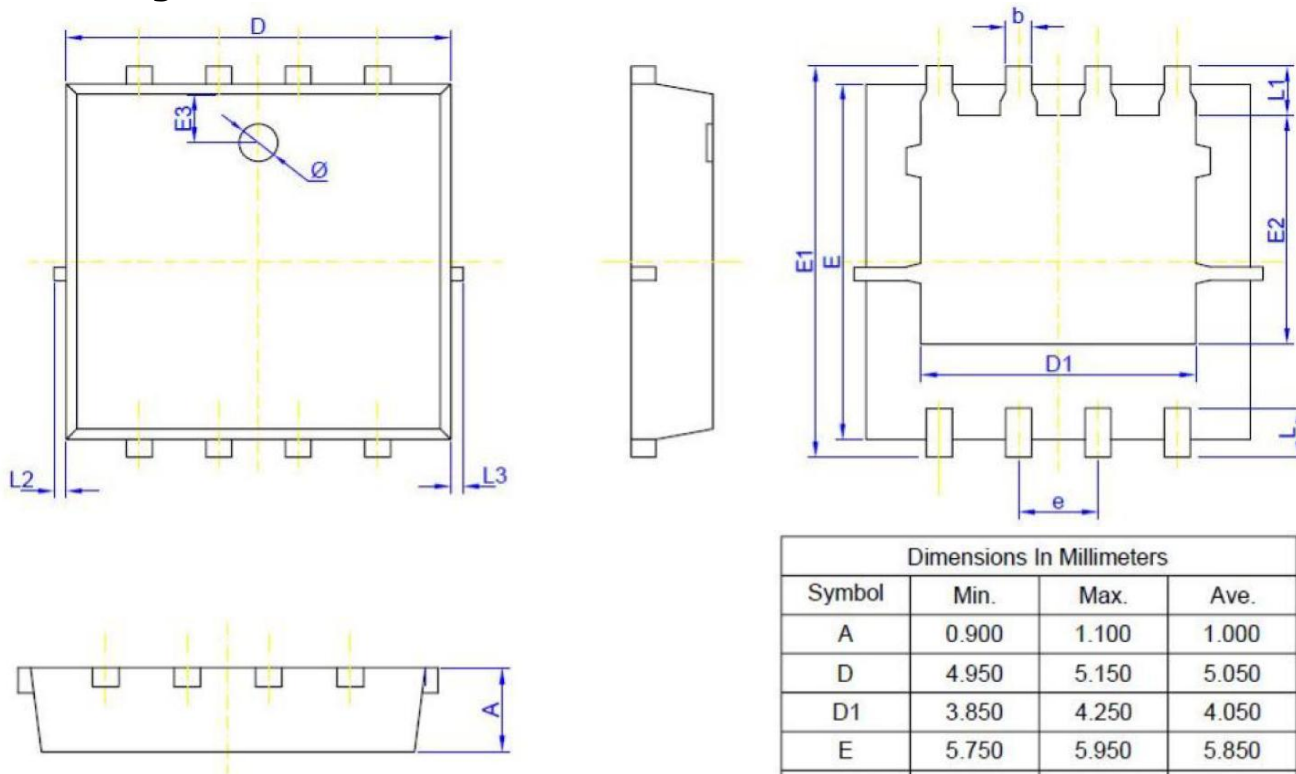
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outline:PDFN5*6



Dimensions In Millimeters			
Symbol	Min.	Max.	Ave.
A	0.900	1.100	1.000
D	4.950	5.150	5.050
D1	3.850	4.250	4.050
E	5.750	5.950	5.850
E1	5.950	6.350	6.150
E2	3.300	3.700	3.500
E3	0.900	1.300	1.100
b	0.250	0.350	0.300
e	1.220	1.320	1.270
L	0.585	0.785	0.685
L1	0.525	0.725	0.625
Ø	1.000	1.400	1.200
L2	0~0.100		
L3	0~0.100		

Revision History:

Revision	Date	Major changes
1.0	2025/8/11	Release of Preliminary version.